

FIG. 2

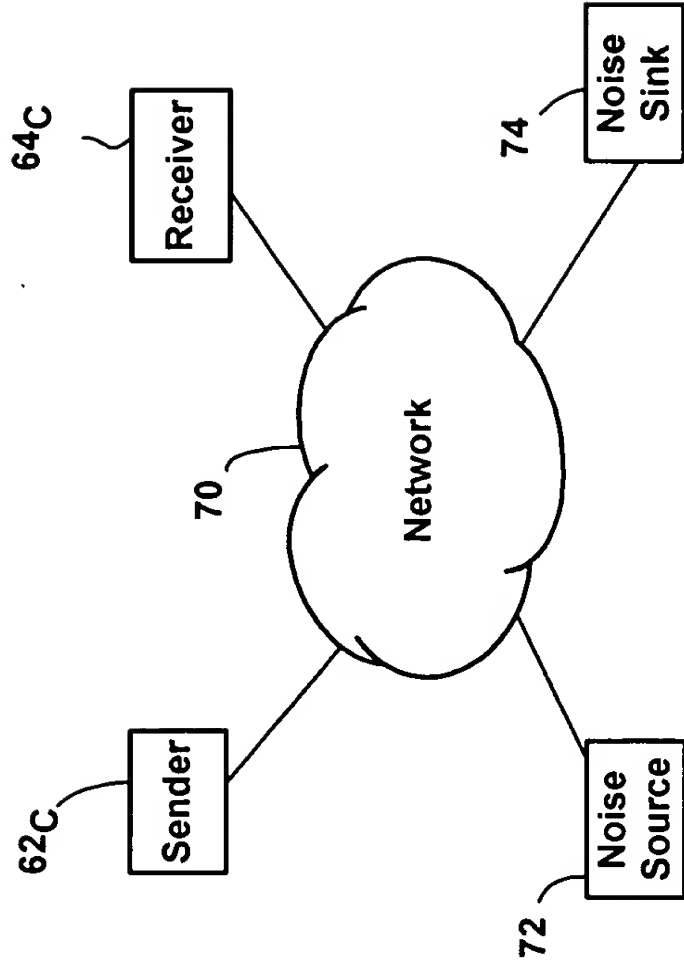


FIG. 3

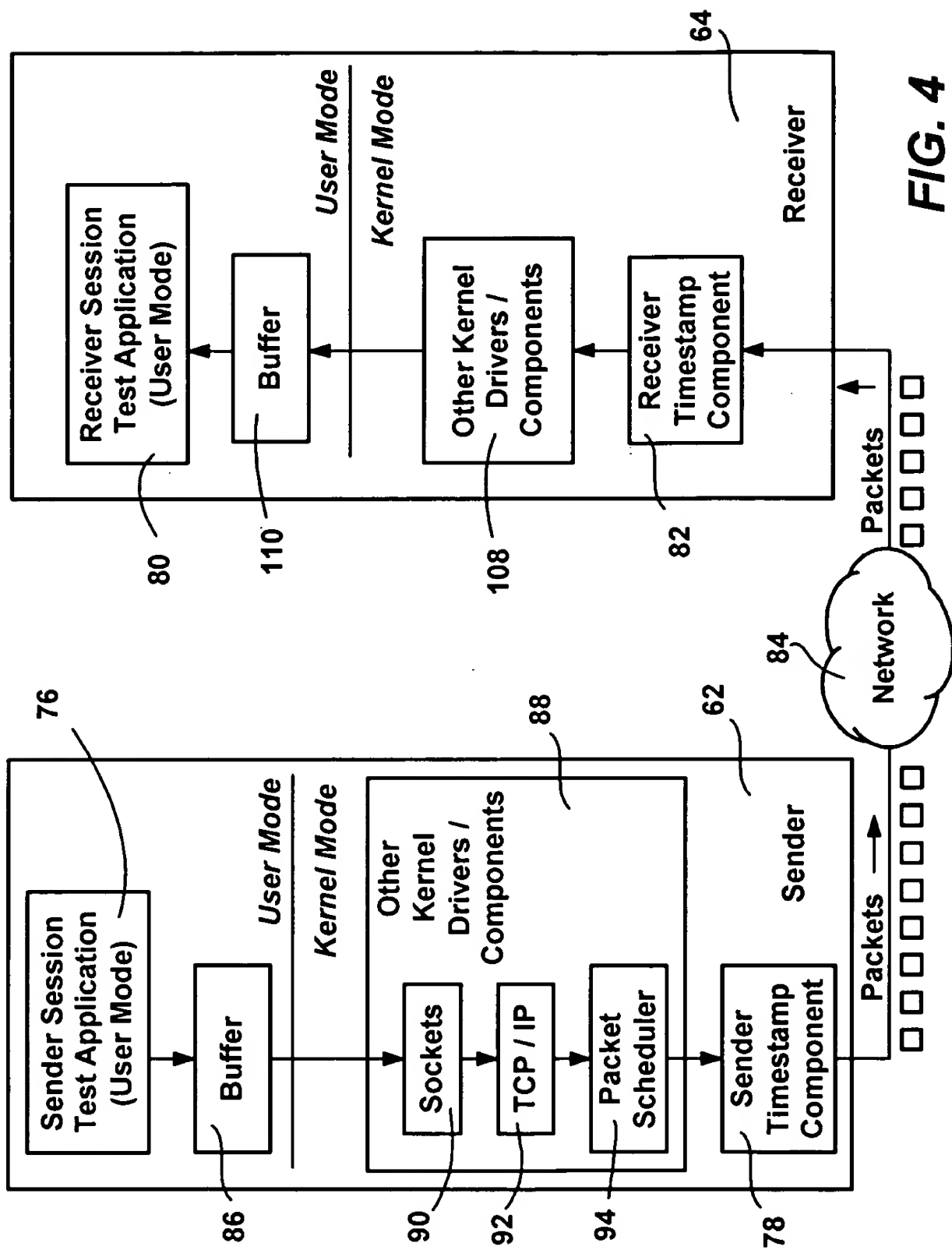


FIG. 4

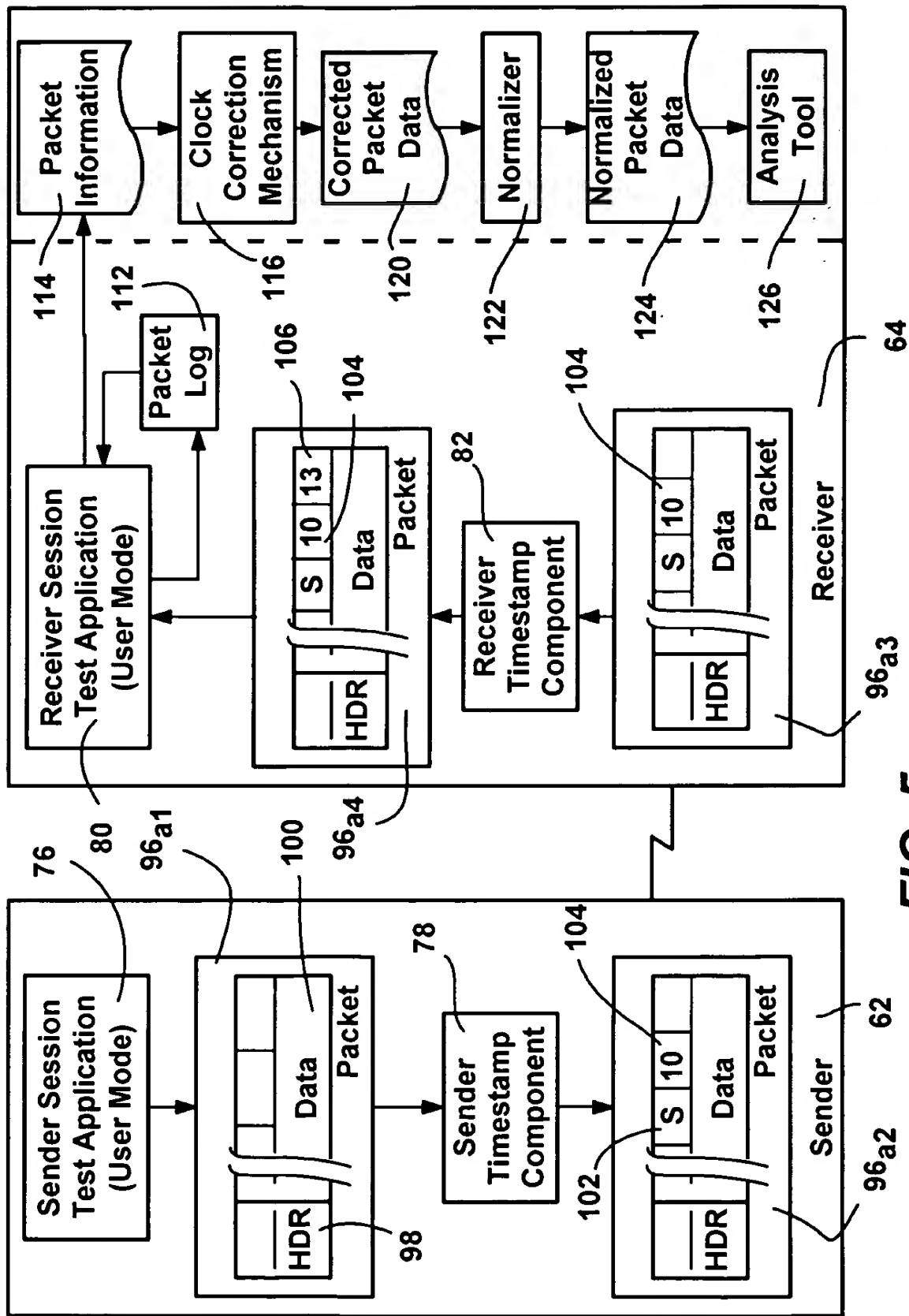


FIG. 5

TimeSent	TimeReceived	Latency
10	13	3
11	19	8
12	18	6

128₁

FIG. 6

TimeSent	TimeReceived	Latency
0	13	3
1	19	8
2	18	6

128₂

FIG. 7

TimeSent	TimeReceived	Latency
0	3	3
1	9	8
2	8	6

128₃

FIG. 8

TimeSent	TimeReceived	Latency
0	0	0
1	6	5
2	5	3

128₄

FIG. 9

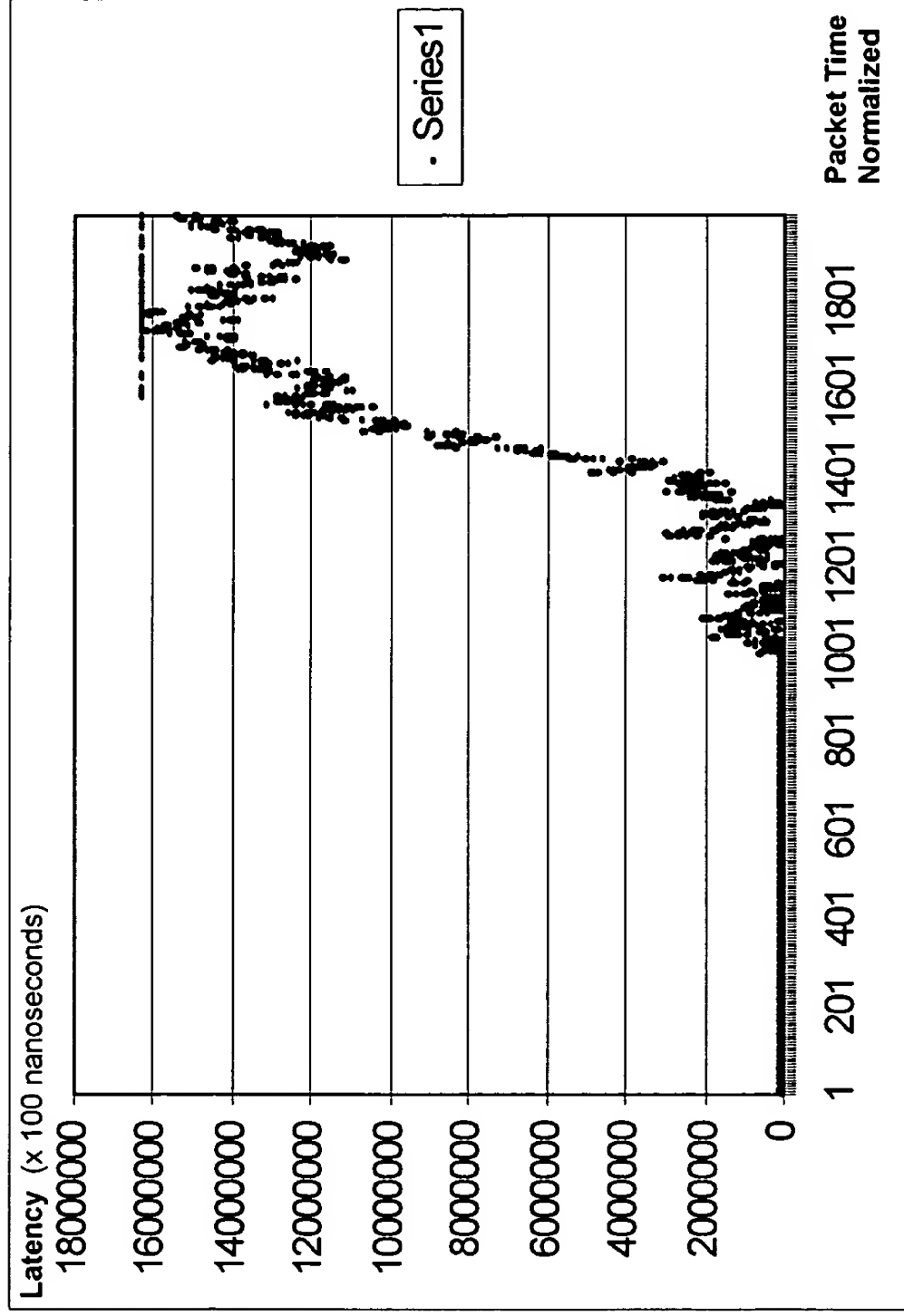
[illegible]

FIG. 10

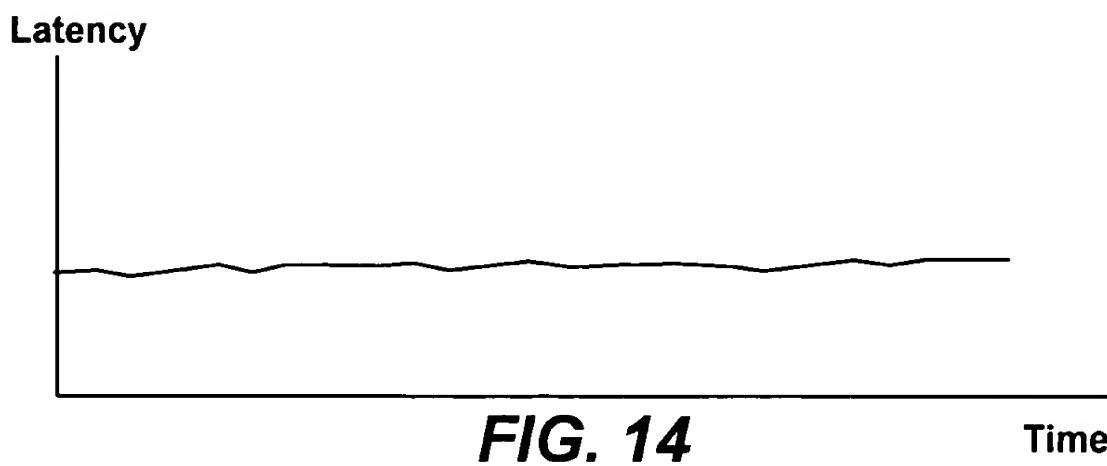
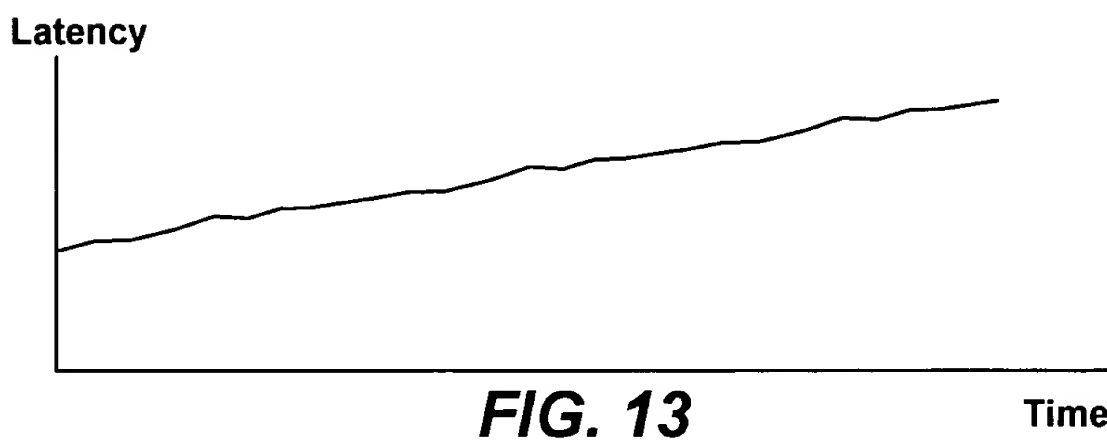
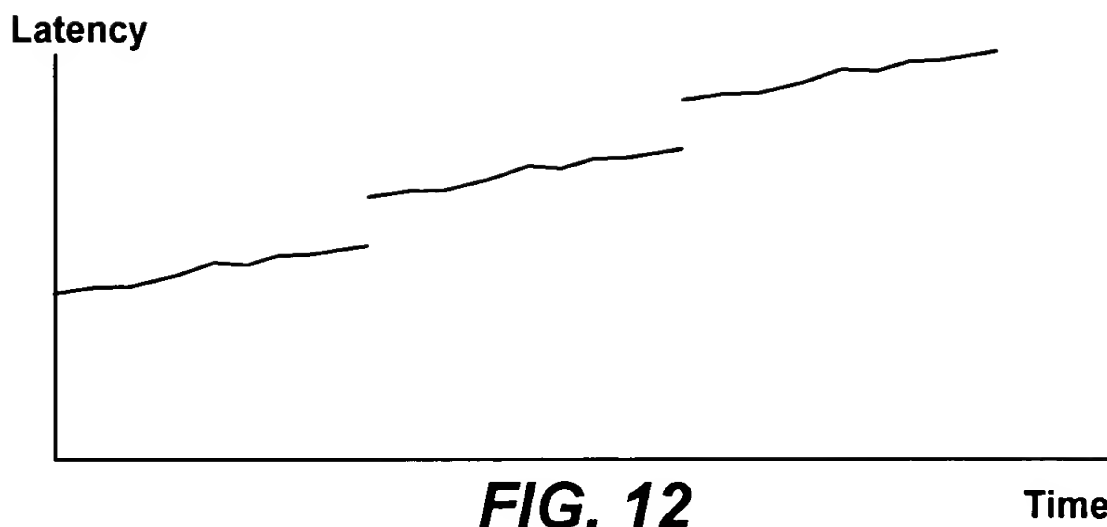


FIG. 15

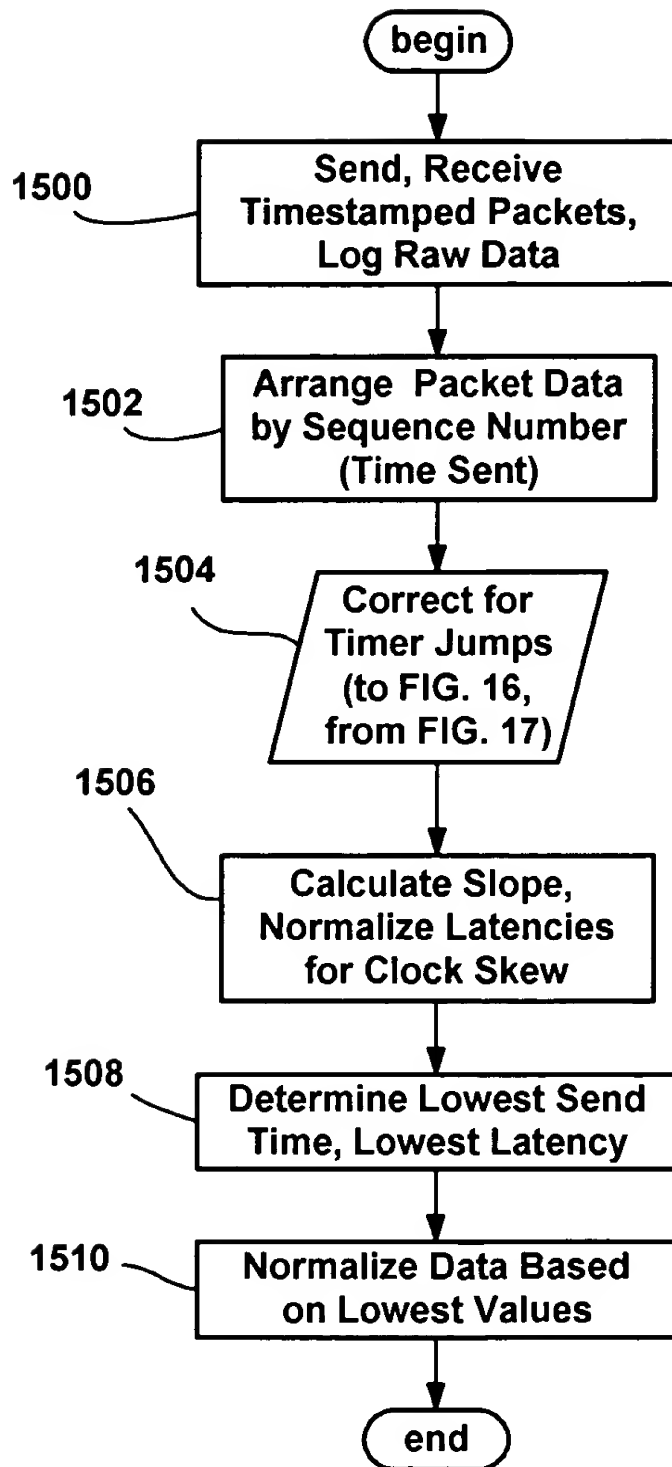


FIG. 16

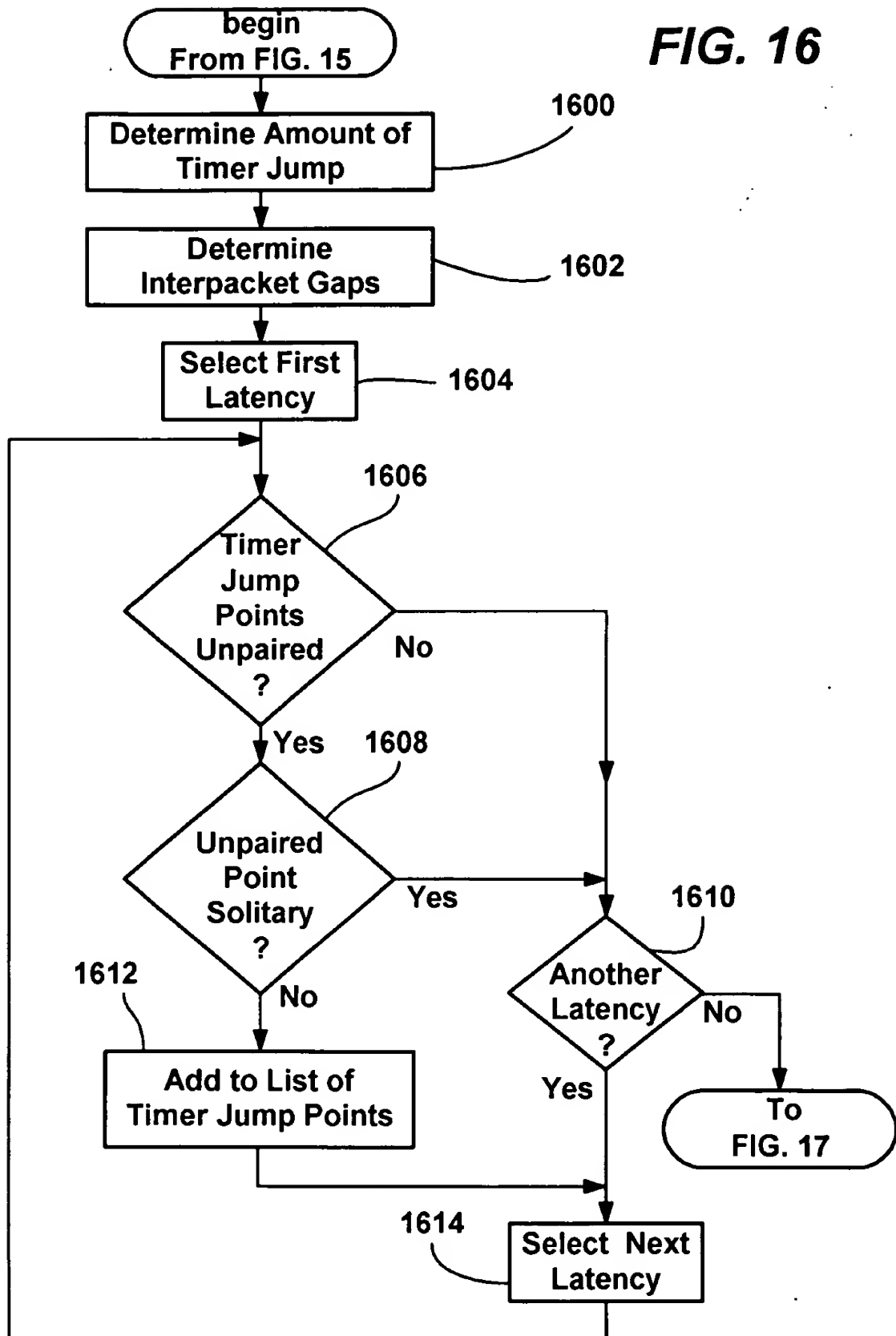


FIG. 17

